

INTER-OFFICE MEMORANDUM

April 28, 1978

From: F. Gruppuso

To : K. Greenberg

cc :	S. Maine	T. Mariner	S. Stuart
	D. Harrower	R. Curth	I. Harris
	R. Stephens	R. Werner	S. Kelly
	F. Jelenko	P. Rush	J. Wunner

Re : CP1600 INSTRUCTION SET

Please note that due to an oversight in generating the original CP1600 Documentation Manual that the list of uninterruptable instructions was and still is incomplete.

As this memo is intended to constitute an official change, it should be included in the next release of that documentation (sometime within the next decade would be preferable).

All combinations of the MVO instructions are uninterruptable. This includes direct, indirect and immediate addressing modes. Uninterruptable in this context means the CPU will not recognize low active enabled signals on the BUSRQ\*, INTR\* or INTRM\* lines for any reason but rather will continue on and execute the next instruction.

Thus, the set of uninterruptable instructions include all MVO, all SHIFTS, EIS, DIS, TCI, CLRC, SETC instructions.

The impact on TIC and STIC software should be carefully considered.

Frank

/do



# LOGIC BOARD PARTS

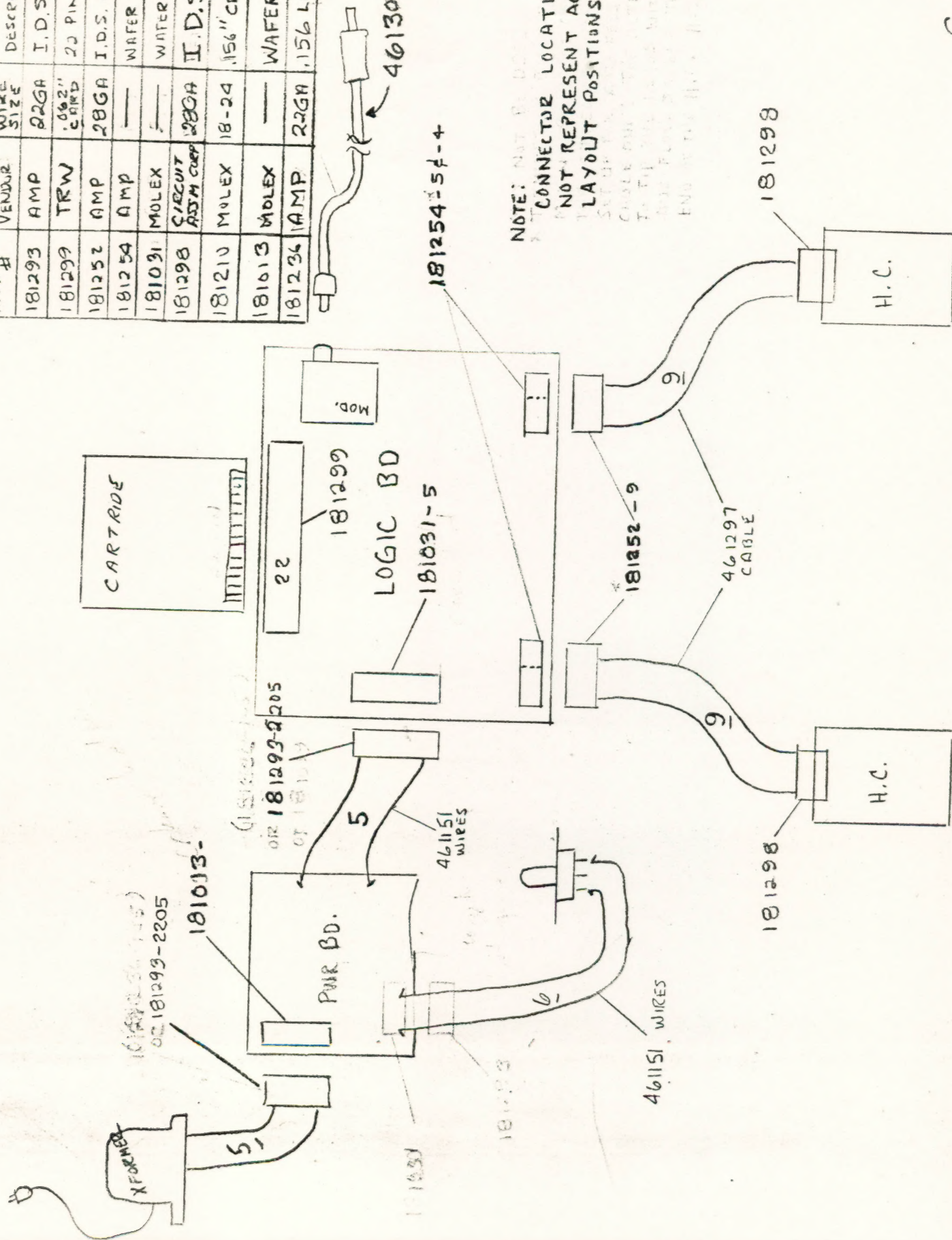
7 29 chips

4 212 RAMS



# MATTEL 2609 SYSTEM INTERCONNECTS SHOWING MAGNIFY PART #'S

MX #	VENDOR	WIRE SIZE	DESCRIPTION
181293	AMP	22GA	I.D.S. CONN. .156"
181299	TRW	.062" CABLE	20 PIN EDGE CARD
181252	AMP	28GA	I.D.S. CONN. .100"
181254	AMP	—	WAFER .100" RT L
181031	MOLEX	—	WAFER .156" RT L
181298	CIRCUIT ASSY M CUP	28GA	I.D.S. CUSTOM
181210	MOLEX	18-24	.156" CRIMP TERMINAL
181013	MOLEX	—	WAFER .156" STRAIGHT
181234	AMP	22GA	.156" LACE LOC





160631-1

2609-9589

593222 - 1



VIDEO



2610

A PRICE

UOTA

ART

DESCRIPTION

# Housing

## ACCESS PANEL

## CASSETTE CASE BASE

ASSETTE CASE LID

# CASSETTE #1 - FOOTBALL

2

 $\frac{1}{3}$ 

5

54

۱۵

NEXT EP

DATE \_\_\_\_\_

# UNITS

PTT.OT

DATE \_\_\_\_\_

## UNITS

## PRODUCT

STAR!

DATE \_\_\_\_\_

8-14

## VIDEO-CARTRIDGE

LET/

PO TO

**VENDOR**

1st

SHOTS/

SAMP

DEFRIC

WEEKS  
LEADS

FOOT

7007

2

THE

1111



	8-25	9-1	9-8	9-15	9-22	9-29	10-6	10-13	10-20	10-27	11-3	11-10	11-17	11-24	12-1
Chip Delivery	2200			5000		5000		10000	5000	5000	5000	7500	7500	7500	
Cum Total				7200		12200		22200	27200	32200	37200	44700	52200	59700	
Scheduled Production	500	500	500	1000	2000	2500	2500	5000	5000	5000	5000	5000	5000	5000	5000
No Overtime	500	1000	1500	2500	4500	7000	9500	14500	19500	24500	29500	34500	39500	44500	49500
With Overtime											6000	7500	7500	7500	7500
											30500	38000	45500	53000	60500
25 Pre-Prod 5/1															
6 Pre-Prod 5/1															
125 to 250 Prod 8/4															

Contract should require that 250 systems could be built end of August and start build up to 1000/day by mid August